

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	09/592106	Docket Number	MIPS.0167-00-US
Filed	6/12/2000	Group Art Unit	2154
Examiner	Larry Donaghue	Customer No.	23669
Application Title	METHOD AND APPARATUS FOR IMPLEMENTING ATOMICITY OF MEMORY OPERATIONS IN DYNAMIC MULTI-STREAMING PROCESSORS		
First Named Inventor	Stephen Melvin		

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop **Missing Parts**
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Filing Receipt dated 08/07/00 incorrectly indicates the Domestic Priority Data. Please correct the Domestic Priority Data. An Application Data Sheet (ADS) was filed online on 04/25/2006.

A copy of the Filing Receipt with corrective markings thereon is attached hereto.

Respectfully submitted,
HUFFMAN LAW GROUP, P.C.
/James W. Huffman/

By: _____

Jim Huffman
Registration No. 35,549
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07/18/06

Date: _____

3813

FILING RECEIPT

OC000000005299683

**UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office**Address: ASSISTANT SECRETARY AND
COMMISSIONER OF PATENT AND TRADEMARKS
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/592,106	06/12/2000	2782	345	P3813	11	12	2

24739
CENTRAL COAST PATENT AGENCY
PO BOX 187
AROMAS, CA 95004

Date Mailed: 08/07/2000

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Applicant(s)Stephen Melvin, San Francisco, CA ;
Mario Nemirovsky, Saratoga, CA ;**Continuing Data as Claimed by Applicant**

THIS APPLICATION IS A CIP OF 09/216,017 12/16/1998
AND A CIP OF 09/240,012 01/29/1999
AND A CIP OF 09/273,810-03/22/1999
AND A CIP OF 09/312,302 05/14/1999
(*) Data inconsistent with PTO records. →

This application is a CIP of patent 6477562 11/05/2002
This application is also a CIP of patent 6292888 09/18/2001
This application is also a CIP of patent 6389449 05/14/2002
This application is also a CIP of patent 7020879 03/28/2006

Foreign Applications

If Required, Foreign Filing License Granted 08/04/2000

**** SMALL ENTITY ******Title**

Method and apparatus for implementing atomicity of memory operations in dynamic multi-streaming processors

Preliminary Class

712

Data entry by : LEE, LINA

Team : OIPE

Date: 08/07/2000



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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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